L Number	Hits	Search Text	DB	Time stamp
1	3	(716/\$).ccls. and (logic adj circuit) and (add\$ adj information)	USPAT;	2003/02/20 10:59
	-	and (passive adj component)	US-PGPUB;	i
ļ	i		, EPO; JPO;	
	i		DERWENT;	
	Į.		IBM_TDB	1
2	o i	(logic adj circuit) and (addition adj information) and (passive	USPAT;	2003/02/20 11:00
		adj component)	US-PGPUB;	1
	į	•	EPO; JPO;	
	1		DERWENT;	
	}		' IBM_TDB	
	1 !	(logic\$ adj circuit) and (addition adj information) and (passive	USPAT:	1 2003/02/20 11:02
	į	adj component)	US-PGPUB;	1
-		•	EPO; JPO;	
			DERWENT;	
l			[!] IBM_TDB	!
	1	(logic\$ adj circuit) and (deletion adj information) and (passive	USPAT;	+ 2003/02/20 11:04
		adj component)	US-PGPUB;	
	1		EPO; JPO;	
İ			DERWENT;	
[IBM_TDB	
1	28	(716/\$).ccls. and ((logical adj circuit) same (file or stor\$))	USPAT;	2003/02/06 16:5
1			US-PGPUB;	
			EPO; JPO;	!
			DERWENT;	1
			IBM TDB	1
1	0	(wiring adj modification) and ((logical adj circuit) same (file or	USPAT;	2003/02/06 18:0
	Ĭ	stor\$))	US-PGPUB;	
		3.5.4//	EPO; JPO;	1
			DERWENT;	
			IBM_TDB	
	0	(716/\$).ccls. and (topology adj table) and (logical adj circuit)	USPAT:	2003/02/07 13:4
	•	(1 10,4).solo. and (topology as) table, and (together as) and any	US-PGPUB;	
			EPO; JPO;	,
			DERWENT;	1
			IBM TDB	
	12	(716/\$).ccls. and (topology) and (logical adj circuit)	USPAT;	2003/02/07 14:3
		(, του φ), σουσε συνα (εερ ενε 3), συνα (ε 3) εν εν ή ε	US-PGPUB;	
			EPO; JPO;	İ
ļ			DERWENT;	
			IBM_TDB	
	129	(topology) and (logical adj circuit)	USPAT;	2003/02/07 14:3
	120	(topology) and (togloal day on ball)	US-PGPUB;	
			EPO; JPO;	
			DERWENT:	
			'IBM_TDB	•
	1	(topology) and (passive adj component) and (logical adj	USPAT;	+ 2003/02/07 14:4
1	்	circuit)	US-PGPUB;	
			EPO; JPO;	1
			DERWENT:	1
ļ			IBM_TDB	
	13	(passive adj component) and (logical adj circuit)	USPAT;	2003/02/07 14:5
		(Farence and country and (regions and an analy	US-PGPUB;	1
		•	EPO; JPO;	
			DERWENT	•
-			IBM_TDB	
	0	((delay adj line) adj (data or information)) and (logical adj	USPAT;	2003/02/07 15:0
-	J	circuit)	US-PGPUB;	İ
			EPO; JPO;	•
			DERWENT:	
			IBM_TDB	
	219	(delay adj line) and (logical adj circuit)	USPAT;	2003/02/07 15:5
	2.0	(comp) and (cognor and on only	US-PGPUB;	:
		İ	EPO; JPO;	•
			DERWENT;	
,		I	IBM_TDB	i

[-	4	(part adj list) and (logical adj circuit)	USPAT;	2003/02/07 15:57
⁻	,		US-PGPUB;	
			EPO; JPO;	1
			DERWENT:	
			IBM TDB	1
ļ i	ol	(logical adj circuit) and ((passive adj component) adj (delection		2003/02/07 17:11
-	١	adj information))	US-PGPUB;	2000/02/01 11:11
		adj information//	EPO; JPO;	į
			DERWENT;	:
			IBM_TDB	*
	9	(logical adj circuit) and (resistance and capacitance) and	USPAT;	2003/02/07 17:13
-	9	(peradj unit adj length)	US-PGPUB:	2000/02/01
		(perauj unit auj ierigin)	EPO; JPO;	
P			DERWENT;	i '
			IBM_TDB	1
	12	(logical adj circuit) and (resistance and capacitance) and (per	USPAT;	2003/02/07 17:42
-	12		US-PGPUB;	2003/02/07 17:42
		adj unit adj length)	EPO; JPO;	i e
			DERWENT;	
			IBM_TDB	1
	1207	(table come (resistance and canacitance \)	USPAT;	2003/02/07 17:44
-	1307	(table same (resistance and capacitance))	US-PGPUB;	2005/02/07 17.44
1	1		EPO; JPO;	
			DERWENT;	1
				1
	00	(746(ft) calc, and (table same (resistance and conscitance \)	IBM_TDB	2003/02/07 17:46
-	68	(716/\$).ccls. and (table same (resistance and capacitance))	USPAT;	2003/02/07 17.40
			US-PGPUB;	1
			EPO; JPO;	i .
			DERWENT;	
		(740(ft) and and (south and the land (south and the land)	IBM_TDB	2002/02/07 19:06
-	57	(716/\$).ccls. and (rout\$ or wir\$) and (table same (resistance	USPAT;	2003/02/07 18:06
		and capacitance))	US-PGPUB;	1
	!		EPO; JPO;	
1			DERWENT;	
	_	(vine come (adit or modifical) and (legisch adicionis)	IBM_TDB	. 2002/02/14 15:14
-	7	(wire same (edit or modify)) and (logical adj circuit)	USPAT;	2003/02/14 15:14
			US-PGPUB:	
			EPO; JPO;	1
			DERWENT;	
1	1	((in adi adition) adi unit) and (laniati adi airavit)	' IBM_TDB	1 2002/02/14 45:46 1
-	0	((wire adj editing) adj unit) and (logic\$ adj circuit)	USPAT;	2003/02/14 15:46
			US-PGPUB;	
			EPO; JPO;	•
	İ		DERWENT;	1
	1	(vites and addition) and (logico addition)	IBM_TDB	1 2002/02/14 15:51
-	1	(wire adj editing) and (logic\$ adj circuit)	USPAT; US-PGPUB:	2003/02/14 15:51
			{ · - · ,	
			EPO; JPO;	
			DERWENT;	
	10	(//a minth and aircraft) and annual fination)	IBM_TDB	2003/02/14 16:49
-	10	((logic\$ adj circuit) adj modification)	USPAT;	2003/02/14 10:49
			US-PGPUB;	
1			EPO; JPO;	τ
			DERWENT;	1
		A to the state of the standard and the standard	IBM_TDB	1 2002/02/44 16:51
-	0	(wire adj editing) and (logical adj circuit)	USPAT;	2003/02/14 16:51
			US-PGPUB; EPO: JPO:	
	1	! !		
			DERWENT;	4
	_		IBM_TDB	2003/02/14 16:53
-	0	(line adj editing) and (logical adj circuit)	USPAT;	2003/02/14 10:03
	1		US-PGPUB;	1
	1		EPO; JPO;	1
			DERWENT;	1
			LOW LLIN	

-	8	(rerouting) and (logical adj circuit)	, USPAT;	2003/02/14 17:55 ,
1		(1227)	US-PGPUB;	ŀ
			EPO; JPO;	1
			DERWENT;	
			IBM_TDB	i
1.	0	(wire adj modification) and (logical adj circuit)	USPAT;	2003/02/14 17:56
	i		US-PGPUB;	
	1		EPO; JPO;	:
			, DERWENT;	, ,
			IBM TDB	, 1

L Number	Hits	Search Text	DB	Time stamp
1	89	(logical adj circuit) and (inductor)	USPAT;	2003/02/20 13:34
			US-PGPUB;	
			EPO; JPO;	
ł			DERWENT;	
	ļ		IBM_TDB	
.	28	(716/\$).ccls. and ((logical adj circuit) same (file or stor\$))	USPAT;	2003/02/06 16:55
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
	0	(wiring adj modification) and ((logical adj circuit) same (file or	USPAT;	2003/02/06 18:02
1		stor\$))	US-PGPUB;	
1		•	EPO; JPO;	
			DERWENT;	
			IBM_TDB	
	0	(716/\$).ccls. and (topology adj table) and (logical adj circuit)	USPAT;	2003/02/07 13:41
	Ū	(, , o, o, o, o, o, o, o, o, o, o, o, o,	US-PGPUB;	
			EPO; JPO;	!
			DERWENT;	
l			IBM_TDB	•
. 1	12	(716/\$).ccls. and (topology) and (logical adj circuit)	USPAT;	2003/02/07 14:32
	'-	(7 10/\$).0010; and (topology) and (togical day should	US-PGPUB:	
İ			EPO; JPO;	1
			DERWENT.	
ĺ			IBM TDB	•
	129	(topology) and (logical adj circuit)	USPAT;	2003/02/07 14:38
-	129	(topology) and (togical adj circuit)	US-PGPUB;	2003/02/07 14.50
ļ			EPO; JPO;	
			DERWENT;	
			4	
	4	(to a stance) and (a serious adjustment) and (legical adj	IBM_TDB	2003/02/07 14:45
-	1	(topology) and (passive adj component) and (logical adj	USPAT;	2003/02/07 14.43
		circuit)	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
	40	(i C	IBM_TDB	2002/02/07 14-55
-	13	(passive adj component) and (logical adj circuit)	USPAT;	2003/02/07 14:55
			US-PGPUB;	
ł			EPO; JPO;	
			DERWENT;	
	_		IBM_TDB	0000/00/07 45 00
-	0	((delay adj line) adj (data or information)) and (logical adj	USPAT;	2003/02/07 15:08
		circuit)	US-PGPUB;	i !
			EPO; JPO;	
			DERWENT;	
1			IBM_TDB	2002/02/07 45 50
-	219	(delay adj line) and (logical adj circuit)	USPAT;	2003/02/07 15:52
			US-PGPUB;	
			EPO; JPO;	1
			DERWENT;	
			IBM_TDB	
-	4	(part adj list) and (logical adj circuit)	USPAT;	2003/02/07 15:57
ĺ			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
1			IBM_TDB	
-	0	(logical adj circuit) and ((passive adj component) adj (delection	USPAT;	2003/02/07 17:11
		adj information))	US-PGPUB;	1
			EPO; JPO;	
			DERWENT;	1
			IBM_TDB	
-	9	(logical adj circuit) and (resistance and capacitance) and	USPAT;	2003/02/07 17:13
		(peradj unit adj length)	US-PGPUB;	
			EPO; JPO;	1
•			DERWENT;	ı
1		· 1	IBM_TDB	

-	12	(logical adj circuit) and (resistance and capacitance) and (per adj unit adj length)	USPAT; US-PGPUB;	2003/02/07 17:42
		adjunit adjunigurj	EPO; JPO; DERWENT;	
	400-	(in the contract of the contra	IBM_TDB	2003/02/07 17:44
-	1307	(table same (resistance and capacitance))	USPAT; US-PGPUB;	2003/02/07 17:44
			EPO; JPO;	i
l			DERWENT;	
i			IBM_TDB	0000/00/07 47:46
! - 	68	(716/\$).ccls. and (table same (resistance and capacitance))	USPAT; US-PGPUB;	2003/02/07 17:46
			EPO; JPO:	!
			DERWENT;	!
	67	(740/0) and (resistance	IBM_TDB	2003/02/07 18:06
-	57	(716/\$).ccls. and (rout\$ or wir\$) and (table same (resistance and capacitance))	USPAT; US-PGPUB;	2003/02/07 10:00
		and capacitanice))	EPO; JPO;	i
			DERWENT;	
	_		IBM_TDB	2002/02/14 15:14
-	7	(wire same (edit or modify)) and (logical adj circuit)	USPAT; US-PGPUB;	2003/02/14 15:14
			EPO; JPO;	!
			DERWENT;	
		4	IBM_TDB	2002/02/44 45:40
-	0	((wire adj editing) adj unit) and (logic\$ adj circuit)	USPAT; US-PGPUB;	2003/02/14 15:46
			EPO; JPO;	
			DERWENT;	
		(1	IBM_TDB	2002/02/14 15:51
-	1	(wire adj editing) and (logic\$ adj circuit)	USPAT;	2003/02/14 15:51
			EPO; JPO;	
			DERWENT;	1
	40		IBM_TDB	2002/02/14 16:40
-	10	((logic\$ adj circuit) adj modification)	USPAT; US-PGPUB;	2003/02/14 16:49
			EPO; JPO;	
			DERWENT;	
	0	(wire adj editing) and (logical adj circuit)	IBM_TDB USPAT;	2003/02/14 16:51
-	U	(wife adj editing) and (logical adj circuit)	US-PGPUB;	2003/02/14 10:51
			EPO; JPO;	
			DERWENT;	
	0	(line adj editing) and (logical adj circuit)	IBM_TDB USPAT;	2003/02/14 16:53
1		into adj calang, and (logical adj chodit)	US-PGPUB;	1
			EPO; JPO;	
			DERWENT;	
	8	(rerouting) and (logical adj circuit)	IBM_TDB USPAT;	2003/02/14 17:55
		(1.5.55	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
! _	0	(wire adj modification) and (logical adj circuit)	USPAT;	2003/02/14 17:56
1			US-PGPUB;	,
			EPO; JPO;	
			DERWENT; IBM_TDB	
-	3	(716/\$).ccls. and (logic adj circuit) and (add\$ adj information)	USPAT;	2003/02/20 10:59
		and (passive adj component)	US-PGPUB;	
			PPO; JPO; DERWENT;	†
•			IBM_TDB	1

-	0	(logic adj circuit) and (addition adj information) and (passive	USPAT;	2003/02/20 11:00
	_	adj component)	US-PGPUB;	
1		, , ,	EPO; JPO;	
!			DERWENT;	
j .			IBM_TDB	
_	1	(logic\$ adj circuit) and (addition adj information) and (passive	USPAT;	2003/02/20 11:02
		adj component)	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	1	(logic\$ adj circuit) and (deletion adj information) and (passive	USPAT;	2003/02/20 11:04
		adj component)	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
	1 .		IBM_TDB	2002/02/20 12:46
-	1	(logical adj circuit) and (part adj list)	USPAT;	2003/02/20 12:46
	}		US-PGPUB; EPO: JPO:	
			DERWENT;	
			IBM_TDB	
	10	(logical adj circuit) and (component adj (list or table))	USPAT;	2003/02/20 13:30
_	10	(logical adj circuit) and (component adj (list of table))	US-PGPUB;	2000/02/20 10:00
			EPO; JPO;	
			DERWENT:	
!	1		IBM TDB	